

Abstract

SENSE AMPLIFIER FOR A MEMORY ARRAY

A sense amplifier which senses whether current is present on a bit line, and generates one logical value if current is present and another logical value if current is not present. As
5 the sense amplifier can be implemented to generate such logical values with a current signal of low strength, memory arrays with correspondingly low drive strength can be implemented. As a result, memory systems which consume minimal power and having high density can be provided. In addition, as the sense amplifiers can operate without any reference signals, the implementation of sense amplifiers may be simplified.